Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	35	stack with spring same semiconductor	USPAT	OR	OFF	2005/03/14 18:40
L2	74	stack with spring same semiconductor	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/03/14 18:40
L3	2	stack with spring same semiconductor same edge	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	OFF	2005/03/14 18:41
L4	1085	(257/686).CCLS.	USPAT	OR	OFF	2005/03/14 18:41
L5	35	l1 and spring	USPAT	OR	OFF	2005/03/14 18:42